FPGA Engineer

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Company: Matchtech

Location: Staffordshire

Category: architecture-and-engineering

Job summary

Want to join a company with over 30 years of experience in designing, manufacturing, and delivering highly bespoke simulator solutions?

Key skills required for this role

FPGA, FPGA Design, ASIC Design, VHDL, Matlab, Simulink.

Important

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Job description

Important: All applicants must be able to obtain Security Clearance *minimum 5 years UK residency*

Working: Possible Flexible and some remote working potentials.

Want to join a company with over 30 years of experience in designing, manufacturing, and delivering highly bespoke simulator solutions?

Join a reputable business who have a number of awards, most recently been named as an Award winner (Gold Award) in MoD Defence Employer Recognition Scheme!

Join a team at an exciting time, at the start of re-defining their systems, with the opportunity to influence and shape the product!

We have a requirement for an FPGA Engineer in the power converters and motors group. We are open to considering candidates with varying levels of FPGA experience.

Reporting to the FPGA Lead, you will join their established multi-Engineer project team that

is challenged with the task of delivering robust and cost-effective designs for real time power and motor control applications.

You will work within an FPGA focused team environment where co-operation, innovation and thoroughness are essential to develop FPGAs that successfully meet project objectives. Training and development opportunities for career progression are available with this role.

Specific responsibilities for the FPGA Engineer role will depend upon your skill set and level of seniority. Typically, you will be responsible for a wide range of FPGA activities covering all stages of the development lifecycle, from requirements through to implementation, verification and validation of programmed devices.

The level of responsibilities below may differ, depending on your level of seniority.

Responsibilities:

FPGA Implementation including requirements capture, RTL coding and physical stages.

Definition of the conceptual structure and architecture of FPGA designs.

Writing and execution of testbench code and other IV&V activities.

Adherence to safety related standards and processes and contributing to their development and maintenance.

Model Based design techniques using Matlab/Simulink modelling and simulation.

Requirements tracking, configuration control, incident reporting and project documentation.

Lab support during hardware development stages.

Inter-team and intra-team communications, including reviewing.

Experience needed:

Degree or higher in electrical/electronic engineering or equivalent.

Proven track record of FPGA/ASIC design.

Comprehensive knowledge of VHDL with practical experience in successful projects.

Experience of model based design using tools such as Matlab/Simulink is preferable.

Capability of working to formal FPGA/ASIC design processes is essential.

Broad knowledge of digital electronics and other areas of engineering. (e.g. systems, analogue/power electronics, mechanical, motors, etc).

Good knowledge of best practice design lifecycle process; gated design phases, design reviews.

Good in front of customers; willing and able to make presentations.

Desirable:

Experience in the Defence / Aerospace or safety regulated environment would be advantageous.

For full information, please get in touch
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manages this role

Matchtech is a STEM Recruitment Specialist, with over 35 years' experience

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