

Jnr - Snr FPGA Design Engineer

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Company: Cubiq Recruitment

Location: Perth

Category: computer-and-mathematical

Jnr - Snr FPGA Design Engineer

Scotland-Perth, Forfar, Montrose

£25k - £65k per annum

Permanent

Unlock Your Future as a Jnr - Snr FPGA Design Engineer in Scotland!! Perth, Forfar, Montrose

Ready to immerse yourself in a world of innovation and cutting-edge technology? We're searching for a Jnr - Snr FPGA Design Engineer to join our spirited team in Scotland! If you're passionate about pushing the boundaries of what's possible and turning ideas into reality, this could be your golden ticket!

QUALIFICATIONS

Degree:

Bachelor's degree in Software, Computer, or Electronics Engineering (Honors 2:1 or better preferred). Electronics degree requires significant programming experience.

Responsibilities:

An opportunity to tackle technically demanding projects, develop new skills

Opportunity to show that you share our key values of Innovation, Commitment, Integrity, Passion and Team Spirit

Join a team full of desire and ambition to be the best we can.

You will be part of a successful team working at the forefront of technology, developing new and innovative concepts for future products

You will be involved in the design, development and proving of key products throughout the product lifecycle from concept to in service support.

Architecture and Requirement definition. Working with hardware and system engineers to define the FPGA needs.

Design implementation and test.

Prepare and efficiently maintain design and test documentation Experience:

Interested in FPGA, digital design or Digital design/ High speed Digital Experience.

Competent VHDL skills.

Competent verification skills where SystemVerilog would be an advantage, but not mandated.

Candidates will need to demonstrate a proven track record of designing for Xilinx, Intel or Microsemi devices and mastery of associated tool chains.

Candidates will have completed a significant number of complex FPGA designs using Xilinx, and/or Altera FPGA devices (including SoC configurations using Power PC or ARM cores) and have knowledge of the trends in FPGA design and technology.

A thorough understanding of the use of design tools such as Xilinx EDK, Xilinx ISE, Xilinx XST, Vivado, Mentor Graphics HDL Designer, Precision Synthesis, ModelSim is also

Requirement. What you get in return:

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This company is an equal opportunity employer and value diversity. They do not discriminate on the basis of race, religion, colour, national origin, gender, sexual orientation, age, marital status, veteran status, or disability status.

**** If you're interested in this opportunity, please submit your CV via the link provided ****

Cubiq Recruitment is recognised as a trusted supplier of permanent, contract and interim recruitment services to AI, Software ERP, Engineering, Manufacturing and Commercial sectors. Our teams of specialist recruiters operate across all core commercial engineering & technology disciplines and specialist areas.

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