**Senior Design Verification Engineer**

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Company: IC Resources

Location: Greater Bristol Area

Category: other-general

ASIC Verification Engineer - BristolHybrid working model and a fantastic compensation package.As a Digital Verification Senior Staff Engineer, you will work within the IP Development team in Bristol, a team with a proven track record in successful IP deliveries into Automotive Microcontrollers.This is an exciting time to join an established Semiconductor in the vibrant city of Bristol. Bristol is a city straddling the River Avon in the Southwest of England with a prosperous maritime history. Its former city-centre port is now a cultural hub, the Harbourside, where the M Shed museum explores local social and industrial heritage. The harbour's 19th-century warehouses now contain restaurants, shops and cultural institutions.In your new role you will:Be responsible for developing System Verilog – UVM testbenches and solve potentially complex problems related to test bench developmentBe responsible for developing right from scratch UVC components for new verification environments;Be responsible for defining and writing a functional coverage model;Debug failing test cases to root cause;Participate in reviews to ensure test bench meets quality and is complete;Contribute to verification perspective in Design and Concept meetings;Ensure test bench meets sign-off targets, including coverage, functional safety and test bench qualification;Proactively help increase efficiency of verification activities and mitigate risks early;Contribute to enhancing Verification strategy and architecture of IP testbenches.Key SkillsAt least 8 years of experience in Verification working with Verilog and/or SystemVerilog;5 years of experience on IP/block level Test-bench bring up on SV UVM based platform;The ability to understand complex design specification, derive features and test bench architectures from concept;Familiarity with CAD/EDA tools for Design and Simulation;Working

knowledge in scripting languages for verification environments (e.g Python, Perl, TCL would be preferred);Strong background in Digital Logic Design and Verification;As a top company you can expect ongoing training and support, flexible working conditions, sabbaticals, health care, private insurance offers and pension plans as well as a compensation package which includes a generous base and bonus scheme.Visa sponsorship can be provided but relocation is a must and remote work is NOT an option for the role.For more information, please contact Rachel Mason at IC Resources.

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