

Senior DFT Engineer

[Apply Now](#)

Company: ARM

Location: GODMANCHESTER

Category: other-general

Job Overview:

Arm's DFT methodology team works on DFT for projects, including soft IP, hard macros, testchips and physical library IP across all the Arm design sites. In addition, this team builds and drives DFT methodology and flows throughout all of Arm and works to get support from EDA vendors to support our methodologies.

Responsibilities:

Support DFT on multiple types of projects in multiple design centers and apply innovative DFT techniques and affect the content of forthcoming CPU, GPU, ML and systems IP, some years before they appear in mainstream products. This candidate will also contribute to DFT methodology by crafting flows, evaluating tool capabilities, helping other specialists on projects, detailing work through documentation, working with EDA vendors and propagating DFT methodologies. This position may also include meeting with customers for DFT training or to address DFT concerns.

Required Skills and Experience :

Experience with Perl, TCL, and/or C programming

Proficiency in Unix/Linux environments

Knowledge of at-speed testing, test insertion and test coverage assessment, test pattern development, scan compression, Memory BIST, Logic BIST, JTAG, IJTAG, fault simulation, debug, verification, designing and conducting experiments/tool evaluations.

Experience with Siemens, Cadence and/or Synopsys DFT tools

“Nice To Have” Skills and Experience :

Ability to build and deploy generic DFT flows

Familiarity with IEEE standards such as 1500, 1149.1, 1687 and 1838

Familiarity with supporting silicon into volume production

Gained some exposure to digital ASIC front and backend design & verification processes

Hands-on Synthesis and Static Timing Analysis (STA) experience

Familiarity with current mobile SOC architectures and low power design practices would be an advantage

Understanding of Functional Safety as it applies to DFT

Confirmed understanding of Siemens MBIST and LBIST tools

Experience in using simulation and formal verification tools

In Return:

Arm offers a competitive benefits package including private medical insurance, sabbatical, supplementary pension, and wellness benefits. We are offering a hybrid approach to home and office working to provide an adaptable experience for all employees and to promote a strong collaborative environment.

#LI-KD1

[Apply Now](#)

Cross References and Citations:

1. Senior DFT Engineer Romejobs Jobs GODMANCHESTER Romejobs ↗
2. Senior DFT Engineer Cinemajobs Jobs GODMANCHESTER Cinemajobs ↗
3. Senior DFT Engineer Topfishjobs Jobs GODMANCHESTER Topfishjobs ↗
4. Senior DFT Engineer Veteranjobs Jobs GODMANCHESTER Veteranjobs ↗
5. Senior DFT Engineer RomaniaajobsJobs GODMANCHESTER Romaniaajobs↗
6. Senior DFT Engineer Searchnzjobs Jobs GODMANCHESTER Searchnzjobs ↗
7. Senior DFT Engineer Seattlejobsearch Jobs GODMANCHESTER Seattlejobsearch ↗
8. Senior DFT Engineer Minejobs Jobs GODMANCHESTER Minejobs ↗
9. Senior DFT Engineer NzjobscareerJobs GODMANCHESTER Nzjobscareer↗
10. Senior DFT Engineer SearchaustralianjobsJobs GODMANCHESTER Searchaustralianjobs↗
11. Senior DFT Engineer Politicaljobs Jobs GODMANCHESTER Politicaljobs ↗
12. Senior DFT Engineer Ecommercejobopportunities Jobs GODMANCHESTER Ecommercejobopportunities ↗
13. Senior DFT Engineer Tunisiajobs Jobs GODMANCHESTER Tunisiajobs ↗
14. Senior DFT Engineer Govcareer Jobs GODMANCHESTER Govcareer ↗
15. Senior DFT Engineer CeojobsJobs GODMANCHESTER Ceojobs↗
16. Senior DFT Engineer FlightattendantjobsJobs GODMANCHESTER Flightattendantjobs ↗
17. Senior DFT Engineer Gynecologistjobs Jobs GODMANCHESTER Gynecologistjobs ↗
18. Senior DFT Engineer Sustainabilityjobs Jobs GODMANCHESTER Sustainabilityjobs ↗
19. Senior dft engineer Jobs Godmanchester ↗
20. AMP Version of Senior dft engineer ↗
21. Senior dft engineer Godmanchester Jobs ↗
22. Senior dft engineer Jobs Godmanchester ↗
23. Senior dft engineer Job Search ↗
24. Senior dft engineer Search ↗
25. Senior dft engineer Find Jobs ↗

