

## Senior Silicon Photonics Engineer

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Company: Oriole Networks

Location: London Area

Category: other-general

At Oriole Networks, we are developing disrupting technologies for ML/HPC systems that will revolutionise Data Centers and HPCs, speed up the AI/ML training/inference, and dramatically reduce energy consumption for a sustainable future. We are looking for a Senior Silicon Photonic Engineer who can apply their knowledge and experience to design, implementation and testing of PICs for optical communication networks. Design, development, and testing of iterative product prototypes, to get a product to manufacture. You will have the opportunity to innovate at both component and system level. This role will include hands-on design, simulation, and testing as well as closely interfacing with fabrication and packaging teams and vendors.

**What you will be doing:** Design and simulate photonic components and system-level functionality and performance. Prototype novel photonic components and architectures. Work closely with the fabrication team and external manufacturing partners for component design, process integration, and performance monitoring. Develop component and system-level optical testing plans and optoelectronic testing setups. Perform hands-on photonic component and optical system-level testing. Work with the product team to transition/scale testing for performance evaluation. Contribute to the expansion of our IP portfolio through patent filing.

**What we need from you:** 5 years of industrial experience in design and optimization of silicon/III-V photonic devices and photonic integrated circuits. Strong tape out experience with photonics foundries. Strong experience in photonic integrated circuits design CAD tools. Strong understanding of planar waveguide optics, both passive components such as waveguides, multiplexers/demultiplexers, tapers, couplers and active such as detectors, modulators, SOAs and lasers. Strong experience on RF/SI

simulation and optimization, from components PCB/TIA/driver/optical components and systems  
Good understanding of CMOS manufacturing process steps and constraints on the design of photonic devices  
Proficient with mainstream photonic components and systems simulation and layout packages such as Lumerical, Synopsis RSoft, IPKISS  
Proficient with Python and scripting languages.  
Proficient with complex optoelectronic lab testing techniques and equipment, necessary to validate designs, including high-speed detectors, oscilloscopes, spectrum analyzers, precision positioning setups  
Collaborative in a multi disciplinary startup environment, ability to work both with local and remote team members as well as with our manufacturing partners  
Capable of coaching and mentoring new or junior engineers;  
Independent, analytical thinker, comfortable in a fast-paced dynamic frontier technology environment,  
Ph.D. with research in silicon photonics or BEng/MSc on relevant subjects  
Benefits: Highly competitive salary  
Generous stock options  
25 days holiday (+ bank holidays)  
Direct business impact with short feedback loop  
Hybrid working  
Relocation assistance & visa sponsorship provided where necessary

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